

**LAB RECORD**

***Course code: 19CSE401 Compiler Design***

***Submitted by***

CH.EN.U4CSE22078

Sandesh Varma

**BACHELOR OF TECHNOLOGY**

IN

COMPUTER SCIENCE AND ENGINEERING

AMRITA VISHWA VIDYAPEETHAM AMRITA SCHOOL OF COMPUTING CHENNAI

October - 2025

BONAFIDE CERTIFICATE



# AMRITA VISHWA VIDYAPEETHAM

# AMRITA SCHOOL OF COMPUTING,CHENNAI

This is to certify that the Lab Record work for 19CSE401 –Compiler Design Subject submitted by **CH.EN.U4CSE22078– Sandesh Varma** in “**Computer Science and Engineering”** is a bonafide record of the work carried out under my guidance and supervision at Amrita School of Computing, Chennai.

Dr. S. Suthir

Associate Professor(SG) Dept. of CSE

Dr. S Sountharrajan

Asso. Prof. & Program Head Dept. of CSE

This Lab examination held on 8 October 2025

INTERNAL EXAMINER 1 INTERNAL EXAMINER

**INDEX**

|  |  |  |
| --- | --- | --- |
| **Lab No.** | **Title** | **Date** |
| **01.** | 1. Implementation of Lexical Analyzer Using Lex Tool 2. Sample Lex Programs | 21-07-2025 |
| **02.** | Program to eliminate left recursion and factoring from the given grammar | 21-07-2025 |
| **03.** | Implementation of LL(1) parsing | 21-07-2025 |
| **04.** | Parser Generation using YACC | 01-08-2025 |
| **05.** | Implementation of Symbol Table | 09-08-2025 |
| **06.** | Implementation of Intermediate Code Generation | 15-09-2025 |
| **07.** | Implementation of Code Optimization Techniques | 15-09-2025 |
| **08.** | Implementation of Target code generation | 06-10-2025 |

Name: Sandesh Varma Date: 21/07/2025

Roll No: CH.EN.U4CSE22078

Experiment No: 1

\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

Aim: To implement Lexical Analyzer Using Lex Tool

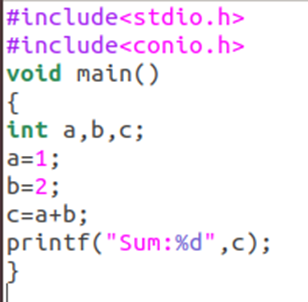
Code:



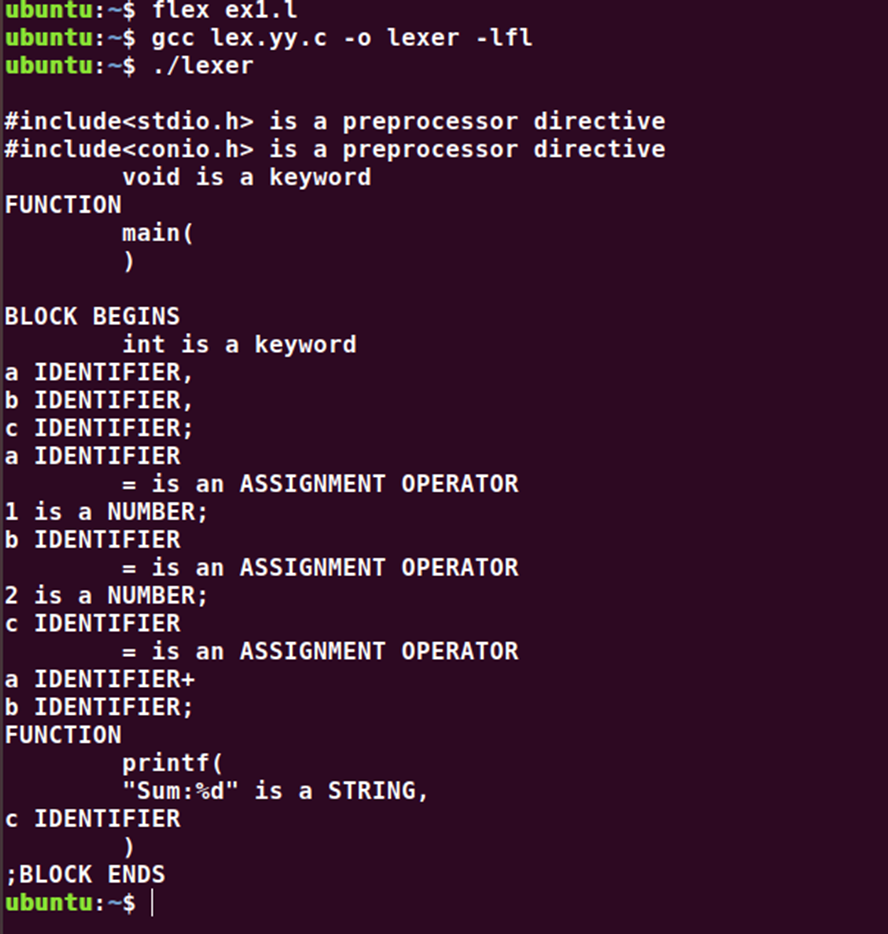
Name: Sandesh Varma

Roll No: CH.EN.U4CSE22078

**Variable.c**



Output:



Result: The code has been executed and output displayed successfully.